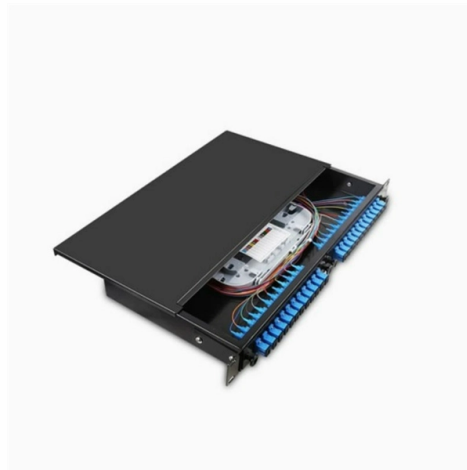


Performance Comparison of Dual-Port Information Panels at 1000mm Depth



Article Overview

Dual-port information panels at 1000mm depth offer trade-offs between latency, throughput, and modularity, with synchronous pipelined designs providing higher clock-to-data speeds and flow-through designs minimizing first-read latency.

Latency and Throughput

Dual-port SRAM panels can operate in synchronous pipelined or flow-through modes. Pipelined synchronous dual-ports introduce a one-clock-cycle latency on the first read but achieve very fast clock-to-data access times, up to 4.2ns at 133MHz, making them ideal for high-speed burst operations. Flow-through synchronous dual-ports, in contrast, provide zero latency on the first read, which is advantageous for applications requiring immediate data access, though sustained throughput may be slightly lower compared to pipelined designs (Renesas AN-254) .

Interface and Connectivity

For large-format or modular panels, interface choice significantly affects performance. Common interfaces include LVDS, DSI, DPI, and eDP:

- LVDS supports long cable lengths and robust high-speed data transmission, suitable for industrial or large-format panels.
- DSI is power-efficient with reduced pin count, ideal for mobile or compact panels.
- DPI is simple and low-cost but requires more wiring and is better for lower-resoluti...

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For more information, pricing, or custom solutions, please contact us:

Website: <https://www.thebucketlistbook.co.za>

Email: sales@thebucketlistbook.co.za

Phone: +33 1 42 78 39 16

Address: 10 Rue de la Paix, 75002 Paris, France

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